

Inventor: Warren M. Farnworth et al.

Amended Title: Computer Including Installable and Removable Cards, Optical  
Interconnect Between Cards, and Method of Assembling a Computer

Assignee: Micron Technology, Inc.

Atty. Docket: MI20-2488

**INFORMATION DISCLOSURE STATEMENT**

**PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98**

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation application of co-pending application Serial No. 09/211,924, filed August 1, 2002. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. §1.98(d) and MPEP §609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 3-12-04



D. Brent Kenady  
Reg. No. 40,045

Form PTO-1449 <sup>1</sup>		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. MI22-2488		PRIORITY SERIAL NO. / 10/211.924	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					APPLICANT Warren Farnworth et al.			
					PRIORITY FILING DATE August 1, 2002		PRIORITY GROUP 2182	
U.S. PATENT DOCUMENTS								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	5,680,361	10/21/97	WARE ET AL.				
	AB	5,663,661	9/2/97	DILLON ET AL.				
	AC	5,621,786	4/15/97	FISCHER ET AL.				
	AD	5,602,667	2/11/97	PATEL				
	AE	5,552,921	9/3/96	HETZEL ET AL.				
	AF	5,546,417	8/13/96	GORELIK				
	AG	5,537,573	7/16/96	WARE ET AL.				
	AH	5,499,385	3/12/96	FARMWALD ET AL.				
	AI	5,499,355	3/12/96	KRISHNAMOHAN ET AL.				
	AJ	5,485,490	1/16/96	LEUNG ET AL.				
	AK	5,446,696	8/29/95	WARE ET AL.				
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AR		"SLDRAM Architectural and Functional Overview _____," Peter Gillingham, SDRAM Consortium, August 29, 1997, pp. 1-14.					
	AS		"Lighting the Way in Computer Design _____," Y.S. Liu, Circuits & Devices, January 1998, pp. 23-31.					
	AT		"Optics: A Maturing Technology for Better Computing _____," Leo Irankliotis et al., IEEE, February, 1998, pp. 36-51.					
EXAMINER					DATE CONSIDERED			
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>								

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. M122-2488		PRIORITY SERIAL NO. 10/211,924	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					APPLICANT Warren Farnworth et al.			
					PRIORITY FILING DATE August 1, 2002		PRIORITY GROUP 2182	
U.S. PATENT DOCUMENTS								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	5,432,823	7/11/95	GASBARRO ET AL.				
	AB	5,430,676	7/4/95	WARE ET AL.				
	AC	5,390,308	2/14/95	WARE ET AL.				
	AD	5,355,391	10/11/94	HOROWITZ ET AL.				
	AE	5,319,755	6/7/94	FARMWALD ET AL.				
	AF	5,119,451	6/2/92	WILLS ET AL.				
	AG	5,009,476	4/23/91	REID ET AL.				
	AH	3,978,343	8/31/76	BRODERICK ET AL.				
	AI	6,331,382 b1	12/18/01	ROBERTSON				
	AJ	6,328,484 B1	12/11/01	UEBBING				
	AK	5,886,374	03/23/99	SAKAMOTO ET AL.				
	AL	5,475,778	12/12/95	WEBB				
	AM	5,144,691	09/01/92	AUGUST ET AL.				
	AN	5,013,247	05/07/91	WATSON				
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AO							
	AP							
	AP							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AR		"Optically Interconnected Parallel Computing Systems", Masatoshi Ishikawa et al., IEEE, February, 1998, pp. 61-75.					
	AS		"Perspectives in Optical Computing", H. John Caulfield, IEEE, February, 1998, pp. 22-25					
	AT		MITKAS, Pericles A., et al., "Optical Processing Paradigms for Electronic Computers", IEEE, pp. 45-51 (2/1998)					
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					PRIORITY FILING DATE August 1, 2002		PRIORITY GROUP 2182	
U.S. PATENT DOCUMENTS								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	5,841,628	11/24/98	ALPERT ET AL.	361	600		
	AB	5,347,384	9/13/94	McREYNOLDS ET AL.	359	118		
	AC	5,237,609	8/17/93	KIMURA	380	3		
	AD	6,249,621 B1	6/19/01	SARGENT, IV., ET AL.	385	24		
	AE	6,273,759	8/2001	PERINO ET AL.	439	631		
	AF	6,052,498	4/2000	PANICCIA	385	14		
	AG	5,513,021	4/1996	KANESHIRO ET AL.	359	15		
	AH	5,479,539	12/1995	GOLDSMITH ET AL.	385	14		
	AI	6,157,966	12/2000	MONTEGOMERY ET AL.	710	8		
	AJ	6,202,110	3/2001	COTE7US ET AL.	710	102		
	AK							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AR		GUILFOYLE, Peter S. et al., "Free-Space Interconnects for High-Performance Optoelectronic Switching", IEEE, pp. 69-75 (Feb. 1998)					
	AS							
	AT							
EXAMINER					DATE CONSIDERED			
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>								